

11/28/00

05722423 112800

Jc685 U.S. PTO

PTO/SB/05 (08-05)
U.S. PTO
05722423

11/28/00

Please type a plus sign (+) inside this box ☐

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

Approved for use through 10/31/2002. OMB 0651-0042
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

UTILITY PATENT APPLICATION TRANSMITTAL

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No.	40171
First Inventor	John R. Baldwin
Title	Fault Interrupter Using Microcontroller for Fault Sensing and Automatic Self-Testing
Express Mail Label No.	

APPLICATION ELEMENTS

See MPEP chapter 600 concerning utility patent application contents.

ADDRESS TO: Assistant Commissioner for Patents
Box Patent Application
Washington, DC 20231

1. <input checked="" type="checkbox"/> Fee Transmittal Form (e.g., PTO/SB/17) <i>(Submit an original and a duplicate for fee processing)</i>	7. <input type="checkbox"/> CD-ROM or CD-R in duplicate, large table or Computer Program (Appendix)
2. <input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27.	8. Nucleotide and/or Amino Acid Sequence Submission <i>(if applicable, all necessary)</i>
3. <input checked="" type="checkbox"/> Specification [Total Pages 17] <i>(preferred arrangement set forth below)</i> <ul style="list-style-type: none">- Descriptive title of the invention- Cross Reference to Related Applications- Statement Regarding Fed sponsored R & D- Reference to sequence listing, a table, or a computer program listing appendix- Background of the Invention- Brief Summary of the Invention- Brief Description of the Drawings (if filed)- Detailed Description- Claim(s)- Abstract of the Disclosure	a. <input type="checkbox"/> Computer Readable Form (CRF)
4. <input checked="" type="checkbox"/> Drawing(s) (35 U.S.C. 113) [Total Sheets 4]	b. Specification Sequence Listing on: <ul style="list-style-type: none">i. <input type="checkbox"/> CD-ROM or CD-R (2 copies); orii. <input type="checkbox"/> paper
5. Oath or Declaration [Total Pages] <ul style="list-style-type: none">a. <input type="checkbox"/> Newly executed (original or copy)b. <input type="checkbox"/> Copy from a prior application (37 CFR 1.63 (d)) <i>(for continuation/divisional with Box 17 completed)</i>i. <input type="checkbox"/> DELETION OF INVENTOR(S) Signed statement attached deleting inventor(s) named in the prior application, see 37 CFR 1.63(d)(2) and 1.33(b).	c. <input type="checkbox"/> Statements verifying identity of above copies
6. <input type="checkbox"/> Application Data Sheet. See 37 CFR 1.76	

ACCOMPANYING APPLICATION PARTS

9. <input type="checkbox"/> Assignment Papers (cover sheet & document(s))	
10. <input type="checkbox"/> 37 CFR 3.73(b) Statement <i>(when there is an assignee)</i>	<input type="checkbox"/> Power of Attorney
11. <input type="checkbox"/> English Translation Document <i>(if applicable)</i>	
12. <input type="checkbox"/> Information Disclosure Statement (IDS)/PTO-1449	<input type="checkbox"/> Copies of IDS Citations
13. <input type="checkbox"/> Preliminary Amendment	
14. <input type="checkbox"/> Return Receipt Postcard (MPEP 503) <i>(Should be specifically itemized)</i>	
15. <input type="checkbox"/> Certified Copy of Priority Document(s) <i>(if foreign priority is claimed)</i>	
16. <input type="checkbox"/> Other:	

17. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below and in a preliminary amendment, or in an Application Data Sheet under 37 CFR 1.76:

<input type="checkbox"/> Continuation	<input type="checkbox"/> Divisional	<input type="checkbox"/> Continuation-in-part (CIP)	of prior application No. _____ / _____
Prior application information: Examiner _____			Group / Art Unit _____

For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 5b, is considered a part of the disclosure of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.

18. CORRESPONDENCE ADDRESS

<input type="checkbox"/> Customer Number or Bar Code Label	(Insert Customer No. or Attach bar code label here)	or	<input type="checkbox"/> Correspondence address below
Name	John E. Holmes		
Address	Roylance, Abrams, Berdo & Goodman, L.L.P.		
	1300 19th Street, N.W., Suite 600		
City	Washington	State	DC
		Zip Code	20036
Country	USA	Telephone	(202) 530-7374
		Fax	(202) 659-9344

Name (Print/Type)	John E. Holmes	Registration No. (Attorney/Agent)	29,392
Signature	<i>John E. Holmes</i>	Date	11/28/00

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Box Patent Application, Washington, DC 20231.

FAULT INTERRUPTER USING MICROCONTROLLER FOR FAULT SENSING AND
AUTOMATIC SELF-TESTING

by

John R. Baldwin

Thomas J. Batko

David F. Ellison

and

Thomas M. McDonald

Field of the Invention:

The present invention relates generally to a fault interrupter, particularly but not necessarily a ground fault circuit interrupter (GFCI), which utilizes a microprocessor to sense
5 faults and to automatically test for proper function of the interrupter on a periodic basis, without the need for external intervention, and to disable the load circuit if an unsafe condition exists.

Background of the Invention:

Conventional GFCI devices are designed to trip in response to the detection of a ground fault condition at an AC load. Generally, the ground fault condition results when a person comes into contact with the hot side of the AC load and an earth ground at the same time, a situation which can result in serious injury. The GFCI device detects this condition by using a sensing transformer to detect an imbalance between the currents flowing in the hot and neutral conductors of the AC supply, as will occur when some of the current on the hot side is being diverted to ground. When such an imbalance is detected, a circuit breaker or relay within the GFCI device is immediately tripped to an open condition, thereby opening both the hot and neutral sides of the AC line and removing all power from the load. Many types of GFCI devices are capable of being tripped not only by contact between the hot side of the AC load and ground, but also by a connection between the neutral side of the AC load and ground. The latter type of connection, which may result from a defective load or from improper wiring, is potentially dangerous because it can prevent a conventional GFCI device from tripping at the intended threshold level of differential current when a line-to-ground fault occurs.

GFCI devices need to undergo periodic testing. Most GFCI devices can be manually tested for proper operation by means of a test further which, when depressed, simulates an actual ground fault by creating a momentary imbalance between the currents flowing in the hot and neutral conductors of the AC power supply. However, most users will use the manual test button occasionally or not at all. Therefore, proposals have been made for GFCI devices which carry out automatic self-tests on a periodic basis, without the need for user-intervention. However, the self-testing GFCI devices proposed so far do not have the ability to directly test for proper operation of the circuit breaker or relay contacts that are intended to remove power from the load in the event of a fault. If the contacts malfunction, power may continue to be available to the load although the GFCI device has taken the necessary steps to create an open circuit condition.

U.S. Patent No. 5,600,524 discloses a ground fault circuit interrupter which incorporates a self test that attempts to check the state of the contacts. The self test involves briefly applying a small amount of current to the contacts through the trip coil. If this small amount of current is measured at the trip coil, the device assumes that the trip coil and thus the contacts will function properly. However, a significant disadvantage to this type of indirect testing is that the current is not measured at the contacts themselves. Accordingly, there may still be current flowing through the contacts after a fault if they are welded closed,

and the load will still be drawing power, resulting in a dangerous condition. This is despite the fact that the GFCI device has properly detected the fault and has attempted to create an open circuit condition at the load. Thus, a need exists for a means of directly testing the circuit breaker or relay contacts of a GFCI device to determine if they have opened and disabled power to the load.

In addition, U.S. Patent No. 5,600,524 discloses complex circuitry to achieve self-testing, fault generation and fault testing. A need exists for less complicated circuitry, thus producing a more efficient and less costly method for ground fault circuit interruption.

10 Summary of the Invention:

In accordance with the present invention, a fault interrupter, such as a ground fault circuit interrupter (GFCI), an arc fault circuit interrupter (AFCI) or a combined GFCI/AFCI device, is provided that can automatically test its internal circuitry on a periodic basis, such as on a monthly, weekly, daily or even hourly basis.

Another aspect of the present invention is to provide a fault interrupter that detects actual imbalances in the load current using a microcontroller.

Another aspect of the present invention is to provide a fault interrupter which operations when a ground fault signal occurs, the load is opened substantially coincident with a zero-crossing of the AC power source.

Another aspect of the present invention is to provide a fault interrupter in which manual testing is accomplished by pushing a manual test button, thus initiating a driver circuit which generates a fault. The fault is sensed by a sense transformer that has an input into the microcontroller, which activates a load relay to an open condition.

An additional aspect of the present invention is to provide a fault interrupter in which the manual test button is a direct input to a microcontroller. When the microcontroller receives such an input, it generates an imbalance in the load current and activates the load relay to an open condition.

Yet another aspect of the present invention is to provide a fault interrupter with the ability to detect when it has been incorrectly wired, that is, when line and load connections have been reversed. Upon detection of a miswiring condition, a visual and/or audible alarm is triggered to alert the user, and the load contacts are opened removing power from downstream electrical devices.

In accordance with yet another aspect of the present invention, a periodic test reminder signal is provided to alert the user to manually test the fault interrupter. A visual

and/or audible alert signal can be generated 30 days after power was initially applied or 30 days after the last manual test was performed. A daylight detector can be included to silence the test reminder signal during evening and nighttime hours.

In accordance with another aspect of the present invention, the microcontroller
5 creates a closed circuit condition if the load circuit becomes open through some external shock or vibration.

The foregoing aspects are substantially achieved by a circuit interrupter apparatus for detecting faults connected to a line monitoring device and a line interrupter circuit. The circuit interrupter apparatus is comprised of a microcontroller connected to a line monitoring
10 device and to a line interrupter circuit, the microcontroller being programmable to receive and process inputs from the line monitoring device to determine the occurrence of a fault in the load, and to operate the line interrupter circuit when a fault is detected.

Another aspect is substantially achieved by a circuit interrupter apparatus for detecting faults in a load connected to a line monitoring device and a line interrupter circuit, wherein
15 the circuit interrupter apparatus comprises a microcontroller and a manual test device. The microcontroller is connected to the line monitoring device and to the line interrupter circuit, and the microcontroller is programmable to receive and process inputs from the line monitoring device to determine the occurrence of a fault in the load and to operate the line interrupter circuit when a fault is detected. The manual test device comprises a manual test
20 switch connected to a driver circuit, the driver circuit being connected to the line monitoring device and being operable in response to activation of the manual test switch to induce a fault condition that is detected by the line monitoring device and indicated to the microcontroller by the inputs.

Another aspect of the present invention is substantially achieved by a circuit
25 interrupter apparatus for detecting faults in a load connected to a line monitoring device and a line interrupter circuit wherein the circuit interrupter apparatus comprises a microcontroller and a manual test switch. The microcontroller is connected to the line monitoring device and to the line interrupter circuit, and the microcontroller is programmable to receive and process inputs from the line monitoring device to determine the occurrence of a fault and to operate
30 the line interrupter circuit when a fault is detected. The manual test switch is connected to an input port of the microcontroller which is programmable to operate the line interrupter circuit in response to activation of the manual test switch.

Another aspect of the present invention is substantially achieved by a circuit
interrupter apparatus for detecting faults in a load connected to a line monitoring device and a
35 line interrupter circuit. The circuit interrupter apparatus comprises a microcontroller

connected to the line monitoring device and to the line interrupter circuit. The microcontroller is programmable to receive and process inputs from the line monitoring device to determine if the line is in open state due to an external condition not relating to the load, and to operate the line interrupter circuit in order to close the line when the open state is detected.

Brief Description of the Drawings:

These and other objects, advantages and novel features of the invention will be more readily appreciated from the following detailed description when read in conjunction with the accompanying drawings, in which

Fig. 1 is a schematic diagram of a GFCI device employing a latching relay and constructed in accordance with a first embodiment of the present invention;

Fig. 2 is a schematic diagram of a GFCI device employing a solenoid-actuated circuit breaker and constructed in accordance with a second embodiment of the present invention;

Fig. 3 is a flow chart illustrating a sequence of operations of a GFCI device using a latching relay in accordance with the first of the present invention; and

Fig. 4 is a flow chart illustrating a sequence of operations of a GFCI device using a solenoid-actuated circuit breaker in accordance with the second embodiment of the present invention.

Throughout the drawings, like reference numerals will be understood to refer to like parts and components.

Detailed Description of the Preferred Embodiments:

Fig. 1 illustrates a ground fault circuit interrupter (GFCI) device constructed in accordance with a first embodiment of the present invention. The voltage source 10 (e.g. a 120 volt AC power supply) is connected in series with a diode 12, a resistor 14, and an SCR 16. These components are connected in series with the resistors 20 and 22. As further explained below, the resistors 20 and 22 allow a microcontroller 60 to trip the GFCI device when the input voltage is close to the zero-crossing of the incoming voltage from the voltage source 10. Modified components can be used instead of the configuration of Fig. 1 to monitor line zero current cross and thus operate the contacts 52 and 54 near the line zero current cross. The microcontroller 60 is preferably a Microchip PIC 16C620A, 621A or 622A type of integrated circuit

chip. SCR 24, a resistor 26 and a capacitor 28 are in series with each other and in parallel with a zener diode 30. These components create a smooth DC input for the microcontroller 60. A sense transformer 32 is connected in series with the resistors 34 and 36, and is further connected as an input to the microprocessor 60 on lines 76 and 78. An additional input to the microcontroller 60 is comprised of a diode 38, and two resistors 40 and 42. These components are coupled with the load terminals 44 and 46, thus allowing the microcontroller 60 to detect if a current exits at the load terminals 44 and 46. The microcontroller 60 has an additional output comprising a light emitting diode (LED) 48 and a resistor 50. As explained in detail below, this allows for external notification of self-tests, malfunctions or other results.

With continued reference to Fig. 1, the operation of the GFCI device 5 will now be described. The AC power source 10 supplies power to the load. If an imbalance in the load occurs, that is, the current to the hot line 33 is not equal and opposite to that on the neutral line 35, the sense transformer 32 sends an analog signal to the microcontroller 60, which in turn produces a digital signal to drive the FET 58. The FET 58 is turned on, thereby actuating the latching relay 56 to create an open circuit condition by opening the contacts 52 and 54. It is important to note that the outputs 76 and 78 of sense transformer 32 are analog signals directly into the microcontroller 60. The typical ground fault chip of the microcontroller 60 can be eliminated by making a delta-sigma converter using one of the microcontroller's 60 analog comparators and one of the microcontroller's 60 analog input/outputs, preferably line 76 or 78.

Additionally, if the GFCI device 5 is put into an open circuit condition in the absence of a fault due to a shock or vibration, the microcontroller 60 directly senses this open condition and closes the contacts 52 and 54 by activating the latching relay 56. Closing of the contacts 52 and 54 is accomplished by the microcontroller 60 sending a signal via line 68 to turn on the FET 58, which is in series with diode 62. Accordingly, the current flows from the collector to the emitter of the transistor 64, thereby driving the latching relay 56 and creating a closed circuit condition at the load 44 and 46.

The GFCI device 5 preferably operates in at least three different modes; a self-test mode, an actual fault mode, and a manual test mode. The self-test mode is initiated automatically and periodically by the microcontroller 60, which provides an approximately 8 milliamp ground fault signal to gate the SCR 16, thus drawing current through resistor 14 and inducing an imbalance in current flow on line 33. The sense

7

Fig. 2 illustrates a GFCI device in accordance with a second embodiment of the present invention. The primary difference between Figs. 1 and 2 is the use of the latching relay 56 of Fig. 1 and the circuit breaker solenoid 70 of Fig. 2. In order to detect a ground fault condition, the GFCI device 15 of Fig. 2 includes a sense transformer 32 through which the AC line and neutral conductors 33 and 35 extend as primary windings. The conductors 33 and 35 also extend as primary windings through an additional transformer 31 which operates as a grounded neutral transformer. The secondary winding 72 of the sense transformer 32 is connected across inputs 76 and 78 of the microprocessor 60. The secondary winding 74 of the grounded neutral transformer 31 is connected across inputs 80 and 82 of the microprocessor 60. As mentioned above, the ground fault chip of the microcontroller 60 can be eliminated using one of the microcontroller's 60 analog comparators and one of the microcontroller's 60 analog input/outputs, preferably lines 80 and 82. In operation, the sense transformer 32 operates as a differential transformer for detecting a current leakage to ground between the load 44 and an earth ground. The grounded neutral transformer 31 detects current leakage to ground between the load 44 and earth ground. If there is no fault condition, then the current will flow through primary windings 33 and 35 with no net flux detected by the core 90 of the sense transformer 32 and the core 92 of the grounded neutral transformer 31. However, if a fault occurs between the hot 44 and neutral lines 46, then the cores 90 and 92 sense a net flux which creates a potential at the outputs 76 and 78 of the sense transformer 32 and 80 and 82 of the grounded neutral transformer 31. These signals are inputs to the microcontroller 60, which energizes the solenoid 70 by means of the resistor 94 and a SCR 96. The solenoid 70 opens the circuit breaker contacts 53 and 55, thus creating an open circuit condition at the load.

Having described the actual fault mode above, the self-test mode of Fig. 2 will now be discussed. The automatic self-test is performed electronically by the microcontroller 60 at some prescribed period of time (e.g., daily, weekly, monthly). Microcontroller 60 electronically produces a ground fault signal, preferably 8ma (half-wave), on line 79 as an input to SCR 16, thus causing it to gate. This in turn produces a net flux between windings 33 and 35, which is further detected by the core 90 of the sense transformer 32 and the core 92 of the grounded neutral transformer 31. This condition creates a potential at the outputs 76 and 78 of the sense transformer 32, as well as outputs 80 and 82 of the grounded neutral transformer 31. These signals are

inputs to the microcontroller 60, which energizes the solenoid 70 by means of the resistor 94 and a SCR 96. The solenoid 70 opens the contacts 53 and 55.

In addition to the self-test and actual fault modes discussed above, the user can perform a manual test. The user depresses a test button 100 that is an input to the microcontroller 60, as seen in Fig. 1. The microcontroller 60 then generates a signal on line 68 and drives the latching relay 56, as detailed above. In the embodiment of Fig. 2, the user of the GFCI device 15 can also perform a manual test by depressing test button 100, thereby creating a shunt across SCR 16 and drawing current through resistor 14 to create an imbalance in the load. The manual test mode occurs preferably during the zero crossing of the AC input voltage waveform in order to extend the life of the contacts and to prevent arcing across the relay contacts 52 and 54 of Fig. 1 and the circuit breaker contacts 53 and 55 of Fig. 2.

In addition, the manual test occurs at the line zero crossing to allow minimal current to flow through to the latching relay 56 of Fig. 1, thus preventing its operation so as not to disturb the load at the terminals 44 and 46. As mentioned previously, more sophisticated circuitry is used to monitor line current zero crossing and operate the contacts 52 and 54 of Fig. 1 and the contacts 53 and 55 of Fig. 2 near the line current zero cross. During a manual test, the microcontroller 60 determines if the contacts 53 and 55 of Fig. 2 have opened. If microcontroller 60 senses current by means of the SCR 38 and resistor 40, then contacts 53 and 55 have malfunctioned and are still closed. Energization of an LED 48 or generation of an audible signal, or both, are performed to notify the end-user.

The GFCI devices of Figs. 1 and 2 contain a reverse line load detector. This detector allows the microcontroller 60 of both embodiments to have substantially continuous power despite miswiring by the outlet installer. The microcontroller 60 is therefore able to notify the end-user of the incorrect wiring. A miswiring occurs when the installer connects the AC source lines 106 and 108 directly to the load lines 44 and 46, bypassing the GFCI device. This condition is not apparent to the user because there is still power at the load. Further, if the test button is depressed, current will flow creating a current imbalance between hot line 106 and neutral line 108 sensed by transformer 32. In Fig. 1, the microcontroller will operate the latching relay 56 to open the contacts 52 and 54. In Fig. 2, the microcontroller 60 energizes the solenoid 70, thus opening circuit breaker contacts 53 and 55. To avoid the appearance to the end user that a proper GFCI device test has been completed, the microcontroller 60 alerts the user of the miswiring using the LED 48 or an audible

signal or both. The microcontroller 60 is able to do this because it is continually receiving power despite the electronics being bypassed by the installer. Therefore, as the microcontroller 60 performs a self-test it will not receive a signal from the transformers 90 and 92, and thus realize a miswiring has occurred.

5 Figs. 3 and 4 describe the operation of the microcontroller 60 when performing a self-test or a manual test, or detecting an actual fault. In Fig. 3, the microcontroller 60 receives an input 200 from the sense transformer 32. The microcontroller 60 determines if one of three situations has occurred: an actual fault 202, an automatic self-test 210, or a manual test 212. If there is an actual fault, the
10 microcontroller 60 outputs a signal to energize the latching relay 56, or the solenoid 70, as seen in 204 of Fig. 3 and 304 of Fig. 4, respectively. The open contacts create an open circuit condition at the hot and neutral load/contacts at 206, and a signal is sent to the network indicating that the contacts are opened at 208.

The self-test mode and the manual test mode are similar. However, as seen in
15 Fig. 1, the manual test button 100 is a direct input into the microprocessor 60. Therefore, in Fig. 3, the microprocessor 60 sends an output to gate the FET 58. In Fig. 4, on the other hand, depressing the test button 100 creates a shunt which in turn gates the SCR 16. As stated previously, the microcontroller 60 notes the imbalance and operates the relay 56 or the solenoid 70. For the solenoid 70, as seen in Fig. 4
20 (block 318), the test occurs just before the zero-crossing of the incoming voltage from the AC source 10 so that there is not enough energy to activate the solenoid, yet enough to draw current through the solenoid 70. Thus, if the microcontroller 60 detects a small amount of current through the contacts 52 and 54, then the GFCI device 15 has been successfully tested. If the microcontroller 60 does not detect a
25 small amount of current, then external notification is given to indicate that the test was unsuccessful at 322. When latching relay 56 of Fig. 1 is used, the microcontroller 60 monitors the current on the load 44 and 46. If there is no current, the contacts 52 and 54 have opened. If there is current, then the contacts 52 and 54 have not opened and the load terminals 44 and 46 are still connected to the source. Accordingly, the
30 microcontroller 60 sends a failure message by activating an LED 48 or generating an audible signal, as shown in block 222.

Preferred values for the electrical components used in the GFCI devices of Figs. 1 and 2 are provided in Table 1 below. Resistor values are expressed in ohms (Ω), kilohms (K), or megaohms (M). Capacitor values are expressed in microfarads

(μ F). Table 1 also expresses the preferred part numbers for the diodes, the FET and the bipolar transistor of Figs. 1 and 2.

Table 1

<u>Component</u>	<u>Value</u>
Resistors 34, 36, 42, 50	1 K
Resistor 14	15 K
Resistors 26, 22, 102, 104	100K
Resistor 40	1 M
Capacitor 28	220 μ F
Capacitor 66	10 μ F
Diodes 12, 24, 38, 102	1N4007
Diode 62	1N4148
Zener Diode 30	1N4699
FET 58	VN2222LL
transistor 64	2N2222A/ZTX

5 In standard GFCI devices, the sense transformer is used to detect load hot to line neutral current which ranges from approximately 4 ma to 6 ma. The grounded neutral transformer is used to detect load neutral to line neutral leakage current, typically ranging from 4 ma to 6 ma. The grounded neutral transformer is used to
10 detect load neutral to line neutral leakage current, typically a minimum of 1.2 ma. A GFCI device can be constructed to use a single transformer to perform both functions. The leakage current from load hot to line neutral generates a sense transformer output voltage which is out of phase with the line hot voltage. Similarly, the leakage current from load neutral to line neutral generates a sense transformer
15 output voltage that is in phase with the line hot voltage. Using the microcontroller 60 to detect the phase of the sense transformer it can be determined whether the leakage current comes from load hot to line neutral, or the load neutral to line neutral, thereby using only one transformer as opposed to two.

20 The above discussion has focused on ground fault circuit interruption. It is important to note that both embodiments of the present invention are applicable to an arc fault circuit interrupter as well. Arc faulting occurs when anomolous currents indicative of arcing are present between two electrical conductors or between an

What is Claimed is:

1. A circuit interrupter apparatus for detecting faults in a load connected to a line monitoring device and a line interrupter circuit, said circuit interrupter apparatus comprising:
a microcontroller connected to said line monitoring device and to said line interrupter circuit, said microcontroller being programmable to receive and process inputs from said line monitoring device to determine the occurrence of a fault in said load, to operate said line interrupter circuit when said fault is detected.
2. A circuit interrupter apparatus for detecting faults in a load connected to a line monitoring device and a line interrupter circuit, said circuit interrupter apparatus comprising:
a microcontroller connected to said line monitoring device and to said line interrupter circuit, said microcontroller being programmable to receive and process inputs from said line monitoring device to determine the occurrence of a fault in a load, and to operate said line interrupter circuit when said fault is detected; and
a manual test device comprising a manual test switch connected to a driver circuit, said driver circuit being connected to said line monitoring device and being operable in response to activation of said manual test switch to induce a fault condition that is detected by said line monitoring device and indicated to said microcontroller by said inputs.
3. A circuit interrupter apparatus as claimed in claim 1 or 2, wherein said load is connected to an AC power source and further comprising detector circuitry configured to open said load thereby generating a ground fault signal at a selected time which is substantially coincident with a zero-crossing of said AC power source, said ground fault signal being provided to said microcontroller to operate said line interrupter circuit and thereby remove power from said load, said microcontroller being programmable to directly monitor said line interrupter circuit, and then initiate said line interrupter circuit to restore power at a selected time relative to a zero-crossing of said AC power source.
4. A circuit interrupter apparatus as claimed in claim 1 or 2, wherein a load is connected to an AC power source and further comprising detector circuitry configured to open said load thereby generating a ground fault signal at a selected time which is substantially coincident with a zero-crossing of said AC power source, said fault signal being provided to said microcontroller to operate said line interrupter circuit, said microcontroller's output comprising sufficient current to test said detector circuitry, but insufficient to activate said line interrupter circuit.

5. A circuit interrupter apparatus as claimed in claim 1 or 2, for detecting faults in a load connected to a line monitoring device and a line interrupter circuit, said interrupter apparatus comprising:

5 an indicator; and

a microcontroller connected to said line monitoring device, said line interrupter circuit and said indicator, said microcontroller being programmable to receive and process inputs from said line monitoring device to determine the occurrence of a fault, and to operate said line interrupter circuit when said fault is detected;

10 wherein said microcontroller is further programmable to operate said indicator to provide notification upon detection by said microcontroller of the occurrence of at least one of a plurality of conditions comprising: successful completion of a self- test, successful completion of a manual test, unsuccessful completion of said self-test, unsuccessful completion of said manual test, the need for a user to initiate said manual test, testing of said
15 line interrupter to ensure proper function thereof, and testing power to said microcontroller to determine whether a reverse line load condition exists.

6. A circuit interrupter apparatus as claimed in claim 5, wherein said indicator comprises an LED.

20

7. A circuit interrupter apparatus as claimed in claim 1 or 2, for detecting faults in a load connected to a line monitoring device and a line interrupter circuit, said circuit interrupter apparatus comprising:

25 a microcontroller connected to said line monitoring device and to said line interrupter circuit, said microcontroller being programmable to receive and process inputs from said line monitoring device to determine the occurrence of said fault, and to operate said line interrupter circuit when said fault is detected; and

a reverse line load detector circuit coupled to said microcontroller to ensure that said microcontroller is receiving power.

30

8. A circuit interrupter apparatus as claimed in claims 1 or 2, wherein:

said microcontroller is programmed to perform at least one self-test wherein said microcontroller operates said line interrupter circuit regardless of said inputs, said ground fault circuit interrupter apparatus conducting said at least one self-test by said microcontroller
35 initiating said fault, said line monitoring device detecting said fault, and said line monitoring

detecting said fault signal at said line monitoring device;
sending an input signal to said microcontroller to initiate said line interrupter circuit;
and

5 sensing a signal from a contact detector circuit, said contact detector circuit having an
input to said microcontroller.

14. A method for detecting faults in a load connected to a line monitoring device and
a line interrupter circuit, comprising operating a manual test device, said method for detecting
faults further comprising:

10 initiating a signal via said manual test device connected to a driver circuit, said signal
inducing a fault condition via said driver circuit;
 sensing said fault condition by said line monitoring device;
 indicating said fault condition to a microcontroller; and
 sensing a signal from a contact detector circuit having an input to said
15 microcontroller.

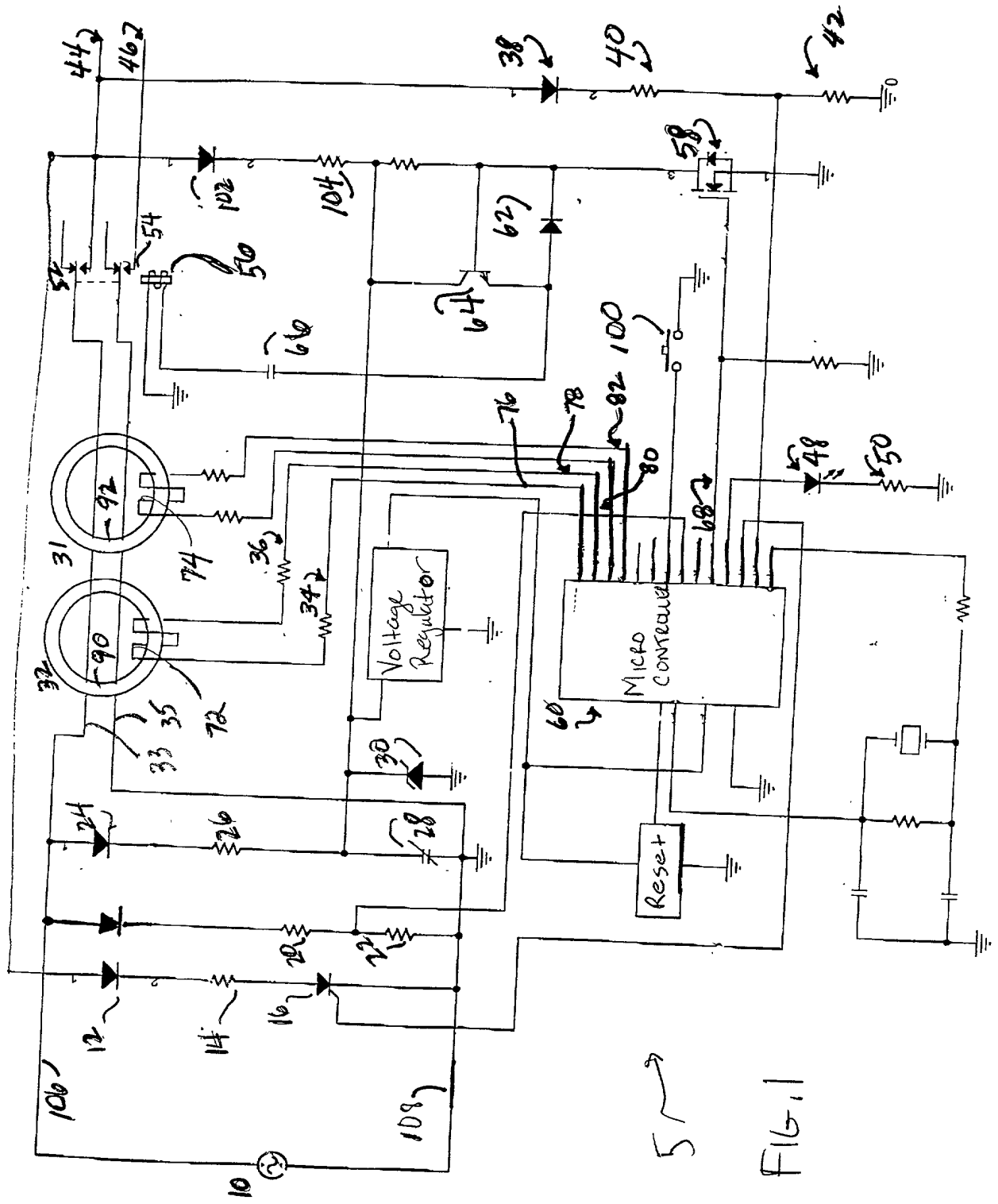
15. A method for detecting faults in an electrical load connected to a line
monitoring device and a line interrupter circuit, said method for detecting faults comprising:
 receiving and processing input signals by a microcontroller in response to activation
20 of a manual test switch coupled to an input of said microcontroller;
 initiating said line monitoring device to generate the occurrence of a fault in said
electrical load; and
 operating said line interrupter circuit when said fault is detected.

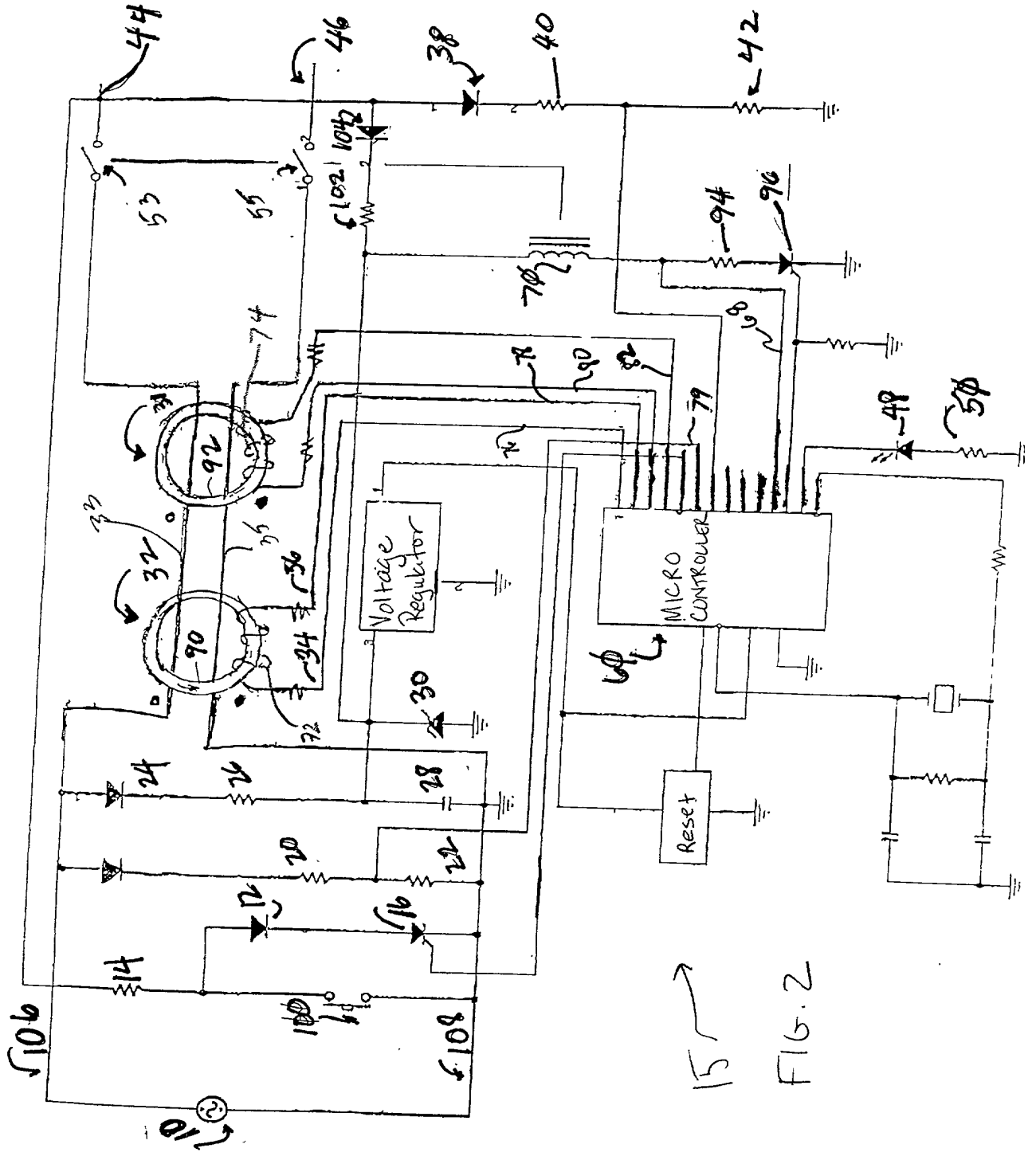
25 16. A method for maintaining power to an electrical load despite external conditions
causing an open circuit state comprising:
 receiving and processing inputs from a line monitoring device by a microcontroller;
 determining if the load is in open state due to an external condition not relating to the
load; and

30 operating a line interrupter circuit in order to restore power to the load when said
open circuit state is detected.

1. *Chlorophyll a* (Chl *a*)
 2. *Chlorophyll b* (Chl *b*)
 3. *Chlorophyll c* (Chl *c*)
 4. *Chlorophyll d* (Chl *d*)
 5. *Chlorophyll e* (Chl *e*)
 6. *Chlorophyll f* (Chl *f*)
 7. *Chlorophyll g* (Chl *g*)
 8. *Chlorophyll h* (Chl *h*)
 9. *Chlorophyll i* (Chl *i*)
 10. *Chlorophyll j* (Chl *j*)
 11. *Chlorophyll k* (Chl *k*)
 12. *Chlorophyll l* (Chl *l*)
 13. *Chlorophyll m* (Chl *m*)
 14. *Chlorophyll n* (Chl *n*)
 15. *Chlorophyll o* (Chl *o*)
 16. *Chlorophyll p* (Chl *p*)
 17. *Chlorophyll q* (Chl *q*)
 18. *Chlorophyll r* (Chl *r*)
 19. *Chlorophyll s* (Chl *s*)
 20. *Chlorophyll t* (Chl *t*)
 21. *Chlorophyll u* (Chl *u*)
 22. *Chlorophyll v* (Chl *v*)
 23. *Chlorophyll w* (Chl *w*)
 24. *Chlorophyll x* (Chl *x*)
 25. *Chlorophyll y* (Chl *y*)
 26. *Chlorophyll z* (Chl *z*)
 27. *Chlorophyll aa* (Chl *aa*)
 28. *Chlorophyll ab* (Chl *ab*)
 29. *Chlorophyll ac* (Chl *ac*)
 30. *Chlorophyll ad* (Chl *ad*)
 31. *Chlorophyll ae* (Chl *ae*)
 32. *Chlorophyll af* (Chl *af*)
 33. *Chlorophyll ag* (Chl *ag*)
 34. *Chlorophyll ah* (Chl *ah*)
 35. *Chlorophyll ai* (Chl *ai*)
 36. *Chlorophyll aj* (Chl *aj*)
 37. *Chlorophyll ak* (Chl *ak*)
 38. *Chlorophyll al* (Chl *al*)
 39. *Chlorophyll am* (Chl *am*)
 40. *Chlorophyll an* (Chl *an*)
 41. *Chlorophyll ao* (Chl *ao*)
 42. *Chlorophyll ap* (Chl *ap*)
 43. *Chlorophyll aq* (Chl *aq*)
 44. *Chlorophyll ar* (Chl *ar*)
 45. *Chlorophyll as* (Chl *as*)
 46. *Chlorophyll at* (Chl *at*)
 47. *Chlorophyll au* (Chl *au*)
 48. *Chlorophyll av* (Chl *av*)
 49. *Chlorophyll aw* (Chl *aw*)
 50. *Chlorophyll ax* (Chl *ax*)
 51. *Chlorophyll ay* (Chl *ay*)
 52. *Chlorophyll az* (Chl *az*)
 53. *Chlorophyll aza* (Chl *aza*)
 54. *Chlorophyll abz* (Chl *abz*)
 55. *Chlorophyll acz* (Chl *acz*)
 56. *Chlorophyll adz* (Chl *adz*)
 57. *Chlorophyll aez* (Chl *aez*)
 58. *Chlorophyll afz* (Chl *afz*)
 59. *Chlorophyll agz* (Chl *agz*)
 60. *Chlorophyll ahz* (Chl *ahz*)
 61. *Chlorophyll aiz* (Chl *aiz*)
 62. *Chlorophyll ajz* (Chl *ajz*)
 63. *Chlorophyll akz* (Chl *akz*)
 64. *Chlorophyll alz* (Chl *alz*)
 65. *Chlorophyll amz* (Chl *amz*)
 66. *Chlorophyll anz* (Chl *anz*)
 67. *Chlorophyll aoz* (Chl *aoz*)
 68. *Chlorophyll apz* (Chl *apz*)
 69. *Chlorophyll aqz* (Chl *aqz*)
 70. *Chlorophyll arz* (Chl *arz*)
 71. *Chlorophyll asz* (Chl *asz*)
 72. *Chlorophyll atz* (Chl *atz*)
 73. *Chlorophyll auz* (Chl *auz*)
 74. *Chlorophyll avz* (Chl *avz*)
 75. *Chlorophyll awz* (Chl *awz*)
 76. *Chlorophyll axz* (Chl *axz*)
 77. *Chlorophyll ayz* (Chl *ayz*)
 78. *Chlorophyll ayz* (Chl *ayz*)
 79. *Chlorophyll azz* (Chl *azz*)
 80. *Chlorophyll azaa* (Chl *aza*)
 81. *Chlorophyll abz* (Chl *abz*)
 82. *Chlorophyll acz* (Chl *acz*)
 83. *Chlorophyll adz* (Chl *adz*)
 84. *Chlorophyll aez* (Chl *aez*)
 85. *Chlorophyll afz* (Chl *afz*)
 86. *Chlorophyll agz* (Chl *agz*)
 87. *Chlorophyll ahz* (Chl *ahz*)
 88. *Chlorophyll aiz* (Chl *aiz*)
 89. *Chlorophyll ajz* (Chl *ajz*)
 90. *Chlorophyll akz* (Chl *akz*)
 91. *Chlorophyll alz* (Chl *alz*)
 92. *Chlorophyll amz* (Chl *amz*)
 93. *Chlorophyll anz* (Chl *anz*)
 94. *Chlorophyll aoz* (Chl *aoz*)
 95. *Chlorophyll apz* (Chl *apz*)
 96. *Chlorophyll aqz* (Chl *aqz*)
 97. *Chlorophyll arz* (Chl *arz*)
 98. *Chlorophyll asz* (Chl *asz*)
 99. *Chlorophyll atz* (Chl *atz*)
 100. *Chlorophyll auz* (Chl *auz*)
 101. *Chlorophyll avz* (Chl *avz*)
 102. *Chlorophyll awz* (Chl *awz*)
 103. *Chlorophyll axz* (Chl *axz*)
 104. *Chlorophyll ayz* (Chl *ayz*)
 105. *Chlorophyll ayz* (Chl *ayz*)
 106. *Chlorophyll azz* (Chl *azz*)
 107. *Chlorophyll azaa* (Chl *aza*)
 108. *Chlorophyll abz* (Chl *abz*)
 109. *Chlorophyll acz* (Chl *acz*)
 110. *Chlorophyll adz* (Chl *adz*)
 111. *Chlorophyll aez* (Chl *aez*)
 112. *Chlorophyll afz* (Chl *afz*)
 113. *Chlorophyll agz* (Chl *agz*)
 114. *Chlorophyll ahz* (Chl *ahz*)
 115. *Chlorophyll aiz* (Chl *aiz*)
 116. *Chlorophyll ajz* (Chl *ajz*)
 117. *Chlorophyll akz* (Chl *akz*)
 118. *Chlorophyll alz* (Chl *alz*)
 119. *Chlorophyll amz* (Chl *amz*)
 120. *Chlorophyll anz* (Chl *anz*)
 121. *Chlorophyll aoz* (Chl *aoz*)
 122. *Chlorophyll apz* (Chl *apz*)
 123. *Chlorophyll aqz* (Chl *aqz*)
 124. *Chlorophyll arz* (Chl *arz*)
 125. *Chlorophyll asz* (Chl *asz*)
 126. *Chlorophyll atz* (Chl *atz*)
 127. *Chlorophyll auz* (Chl *auz*)
 128. *Chlorophyll avz* (Chl *avz*)
 129. *Chlorophyll awz* (Chl *awz*)
 130. *Chlorophyll axz* (Chl *axz*)
 131. *Chlorophyll ayz* (Chl *ayz*)
 132. *Chlorophyll ayz* (Chl *ayz*)
 133.

5 interrupter generates the test signal at a selected time to substantially coincide with the zero-crossing of the AC power source. A manual test can also be performed using a manual test switch provided as a direct input to the microcontroller.





15

FIG. 2

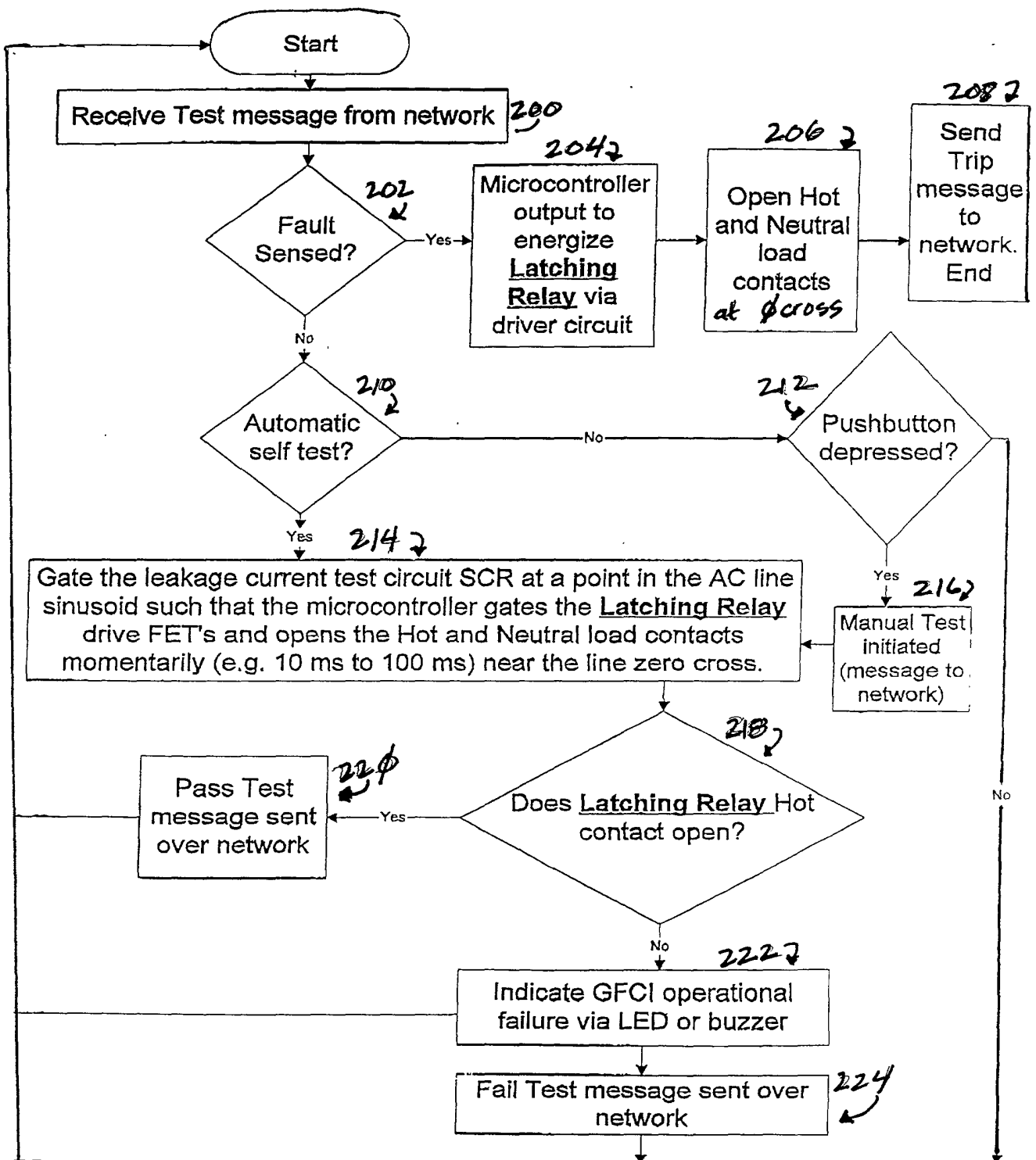


Fig 3

```

graph TD
    Start([Start]) --> Fault{Fault Sensed?}
    Fault -- Yes --> 304[Microcontroller output to energize Solenoid via driver circuit]
    Fault -- No --> 310{Automatic self test?}
    304 --> 306[Open Hot and Neutral load contacts at 0 cross]
    306 --> 308[End]
    310 -- Yes --> 314[Gate the leakage current test circuit SCR at a point in the AC line sinusoid such that the microcontroller gates the Solenoid SCR near an approaching zero cross.]
    310 -- No --> 312{Pushbutton depressed?}
    312 -- Yes --> 314
    312 -- No --> 318{Does Solenoid current monitor detect momentary current flow?}
    314 --> 318
    318 -- Yes --> Start
    318 -- No --> 322[Indicate GFCI operational failure via LED or buzzer]
    322 --> Start

```

FIG 4'